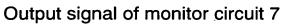


^{3/9} FIG. 3



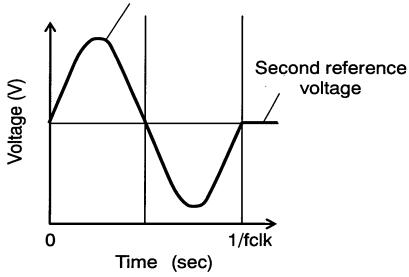
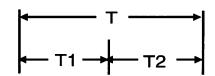
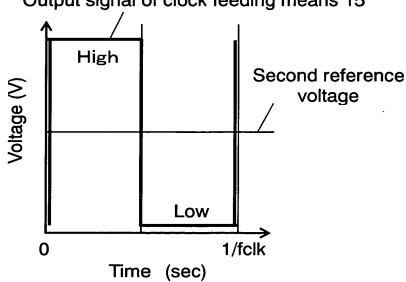
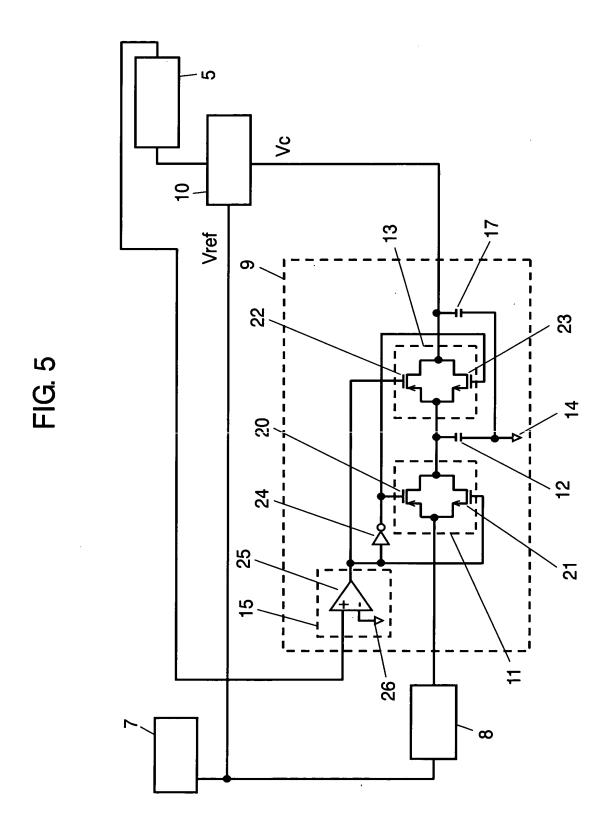


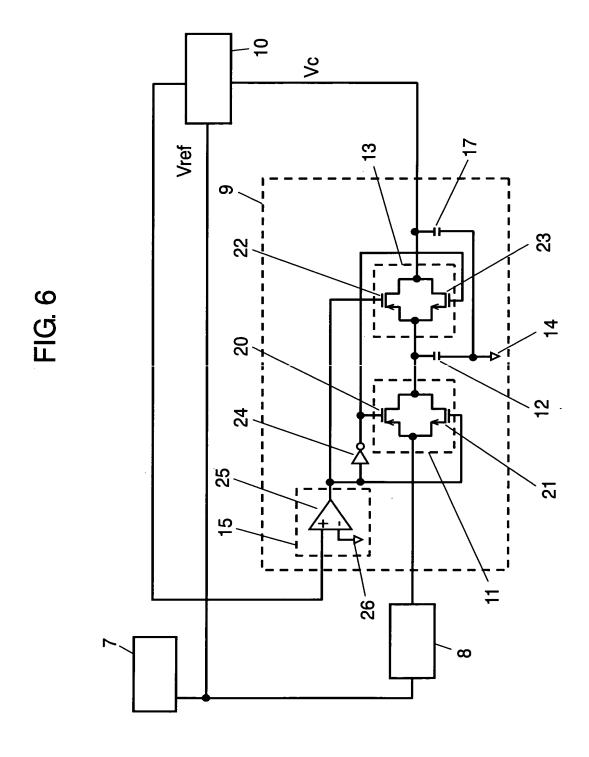
FIG. 4

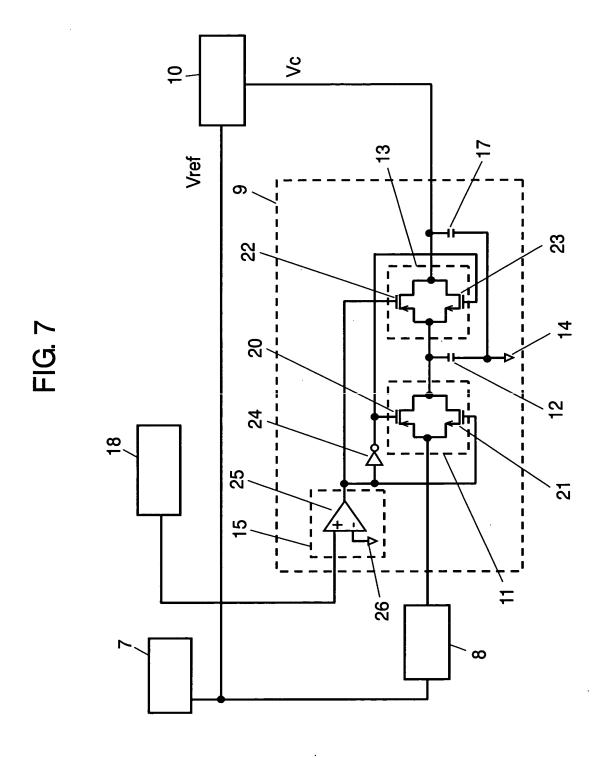


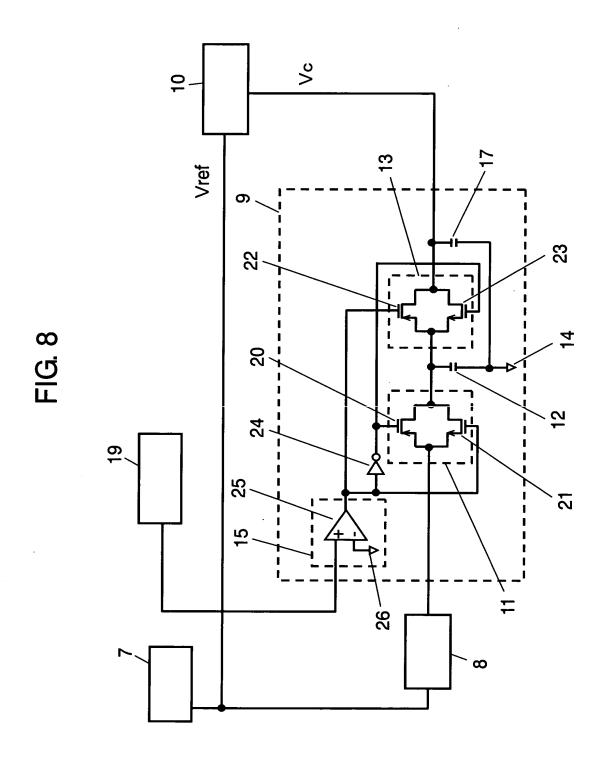
Output signal of clock feeding means 15











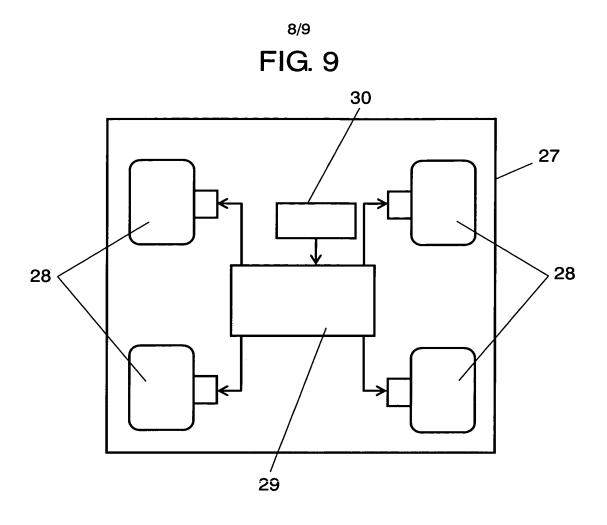


FIG. 10

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FIG. 11

